MITSUBISHI

AJ71QC24N (-R2, R4) Serial Communications Module

MITSUBISHI

General-Purpose PROGRAMMABLE LOGIC CONTROLLER User's Manual (Hardware)

Thank you for buying the Mitsubishi general-purpose programmable logic controller MELSEC-QnA Series.

Prior to use, please read this manual thoroughly and familiarize yourself with the product.



| MODEL | AJ71QC24N-U-H/W-E |
|---------------|-------------------|
| MODEL CODE | 13JL12 |

IB-66765-C (9810) MEE

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● SAFETY PRECAUTIONS●

(Read these precautions before using.)

When using Mitsubishi equipment, thoroughly read this manual and the associated manuals introduced in the manual. Also pay careful attention to safety and handle the module property.

These precautions apply only to Mitsubishi equipment. Refer to the CPU module user's manual for a description of the PLC system safety precautions.

These SAFETY PRECAUTIONS classify the safety precautions into two categories: "DANGER" and "CAUTION"



Depending on circumstances, procedures indicated by ACAUTION may also be linked to senous results.

In any case, it is important to follow the directions for usage.

Store this manual in a safe place so that you can take it out and read it whenever necessary. Always forward it to the end user.

[DESIGN PRECAUTIONS]

 When the notification function is used, a situation may arse in which the pager receiver, cellular phone or PHS cannot be paged due to the radio wave transmission conditions associated with the system installation environment, or an error on the receiver side.
 For the security of the PLC system, provide a separate paging circuit using a lamp indicator or buzzer sound.

[DESIGN PRECAUTIONS]

When performing the control of the PLC in operation (especially changing data, program and operation status (status control)) by connecting a personal computer, etc. to the special function module, configure an interlock circuit in a sequence program so the safety of the overall system is always maintained. Especially, when this control is performed to a remote PLC from an external device, troubles that have occurred on the PLC side may not be able to immediately be handled if there is a data communication error. Define a troubleshooting agreement between external devices and the PLC CPU for data communication error occurrences, as well as

construct an interlock circuit in the sequence program.

 Do not bunch the control wires or communication cables with the mein circuit or power wires, or install them close to each other.
 They should be installed 100 mm (3.9 inch) or more from each other.
 Not doing so could result in noise that would cause malfunction.

[INSTALLATION PRECAUTIONS]

| ACAUTION | | | | | |
|---|--|--|--|--|--|
| Use the PLC in an environment that meets the general specifications contained in this manual. Using this PLC in an environment outside the range of the general specifications could result in electric shock; fire, malfunction, and damage to or detenoration of the product. Switch all phases of the external power supply off when installing or | | | | | |
| placing wring. Not doing so could result in electric shock or damage to the product. | | | | | |
| Insert the tabs at the bottom of the module into the mounting holes in the base module. If the module is not property installed it may result in malfunction, failure | | | | | |
| Tighten the screw within the range of specified torque. | | | | | |
| If the screw are loose, it may result in fallout, short circuit or malfunction. | | | | | |
| Tightening the screws too far may cause damage to the screw and /or the module, resulting in fallout, short circuit or malfunction. | | | | | |
| Do not directly touch the module's conductive parts or electronic components. Doing so could cause malfunction or failure in the module. | | | | | |
| Perform correct pressure-displacement, cnmp-contact or soldering for wire connections using the tools specified by the manufactures. Attach connectors to the module securely. | | | | | |
| WIRING PRECAUTIONS] | | | | | |
| ACAUTION | | | | | |

- Be sure to fix communication cables leading from the module by placing them in the duct or clamping them. Cables not placed in the duct or without clamping may hang or shift, allowing them to be accidentally pulled, which may result in a module malfunction and cable damage.
- Before connecting the cables, check the type of interface to be connected.
 Connection, or erroneous wring to the wrong interface may damage the module and external device.

[WIRING PRECAUTIONS]

- When connecting an external device to AJ71QC24N-R4 RS-422 interface of this module, do not connect a device that must receive power from AJ71Q24N-R4.
 The module or external device may be damaged.
 Tighten the terminal screw within the range of specified torque. If the screws are loose it may result in short circuit or malfunction.
 Tightening the screws too far may cause damage to the screw and/or the module, resulting in failout, short circuit or malfunction.
 When detaching the communication cable from the module, do not pull the cable portion. For cables with connectors, hold the connectors, first loosen the screw at the junction, then detach the cable.
 Pulling the cable portion while it is connected to the module may cause a malfunction or damage to the module and cable.
- Be sure there are no foreign substances such as sawdust or wiring debns inside the module.
 Such debns could cause fire, damage or malfunction.

[STARTING AND MAINTENANCE PRECAUTION]

OANGER

- Do not touch the terminals while the power is on.
 Doing so may cause malfunction.
- Switch all phases of the external power supply off before cleaning or retightening screws. If you do not switch off the external power supply, it will cause failure or malfunction of the module. If the screws are loose, it may result in fallout, short circuits, or wring debns inside the module. Such debns could cause fires, damage, or erroneous operation.

- Do not disassemble or modify the modules.
 Doing so could cause failure, malfunction, injury or fire.
- Switch all phases of the external power supply off before mounting or removing the module. If you do not switch off the external power supply, it will cause failure or malfunction of the module.

[OPERATION PRECAUTIONS]

 Do not write data into the "system area" of the buffer memory of special function modules. Also, do not output the "prohibited to use" signal as the output signal to a special function module from the PLC CPU. Writing data into the "system area" or outputting a signal for "prohibited to use" may cause system malfunction in the PLC.

ACAUTION

 Before performing the control of the PLC in operation(especially changing data, program and operation status(status control)) by connecting a personal computer, etc. to the special function module, read user's manual carefully and confirm if the overall safety is maintained.

Failure to perform correct operations to change data, program or the status may result in system malfunction, machine damage or an accident.

 When using the module while values, such as buffer memory set values, are registered in the EEPROM, do not turn off the power supply for the module loading station nor reset the PLC CPU.
 If the power supply for the module loading station is station is turned off or the PLC CPU is reset while any values are registered, the data contents in the EEPROM become inconsistent and as a result the value must be

set again in the buffer memory, etc. and registered to the EEPROM. Also, this may cause failure and malfunction of the module.

[DISPOSAL PRECAUTIONS]

ACAUTION

· When disposing the product, treat it as industrial waste.

About This Manual

The following product manuals are available. Please use this table as a reference to request the appropriate manual as necessary.

| Manuel Names | Manual No. (Model Code) | | |
|---|------------------------------------|--|--|
| Senal Communication Module Guide Book | IB-66622 (13JF11) | | |
| Senal Communications Module User's Manual | IB-66612 (-C or later) (13J825) | | |
| Senal Communications Module User's Manual Additional Explanation for AJ71QC24N (-R2, R4) type Senal Communications Module (For the manual earlier than IB-66612-B) | SH-3630 (-B or later) (13JL13) | | |
| Computer Link Module Guide Book | SH3510 (13JE76) | | |
| Computer Link Module (Com. link func./ Print. func.) User's Manual | SH-3511 (13JE77) | | |

Related Manual

When using this module, be sure to read Senal Communication Module User's Manual

Correspondence to EMC DIRECTIVE

To make the PLCs compliant with the EMC directive, refer to Chapter 2 "EMC AND LOW-VOLTAGE DIRECTIVE" in the PLC user's manual (Hardware).

* When the PLC CPU user's manual (Hardware) does not include Chapter 2 "EMC AND LOW-VOLTAGE DIRECTIVE", refer to QnA Senes CPU Compatible High-Speed Access Basic Base Unit -Additional Explanation for Product Conforming to EMC Standards (IB-68837) (optional).

1. Overview

This manual is to perform installation and wring to the external device. for the following serial communication modules.

After unpacking the module, check that the following products are included:

| Model | Product Name | Qty. |
|--------------|---|------|
| AJ71QC24N | AJ71QC24N senal communications module | 1 |
| | RS-422 communication terminal resistor 330Ω 1/4W (orange/orange/brown/□) | 2 |
| | RS-485 communication terminal resistor 110Ω 1/2W (brown/brown/Drown/D) | 2 |
| AJ71QC24N-P2 | AJ71QC24N-R2 senal communication module | 1 |
| AJ710C24N-R4 | AJ71QC24N-R4 senal communication module | 1 |
| ~~ | RS-422 communication terminal resistor 330Ω 1/4W (orange/orange/brown/□) | 2 |
| | RS-485 communication terminal resistor 1 10Ω 1/2W (brown/brown/brown/□) | 2 |

* Unless there is a need to identify each device, all of the modules are referred to as "QC24N"

* Differentiate the terminal resistor as follows:





Point

Using the QC24N RS-232C interface (1 channel only), the communication via public line, etc., is possible using the modern function which communicate with the external device. And the date manufacture of the QC24N is as follows.

| | | AJ710C24N-F2 | AJ71QC24N-R4 |
|-----------------|-------------|----------------|--------------|
| Modern function | (Products 9 | 804 or later)* | (Unusable) |

Manufacture date _____ Function version The products with <u>9804</u> [] or later printed on the package display and the date column on rated plate of the module are the products with the modern function added.

(The function versions are written on only the products B version or later)

2. Transmission Specifications

The transmission specifications of the QC24N is given below. Refer to CPU module User's Manual to be used for QC24N general specification.

2.1 When the modern function is not used.

The table below lists the transmission specification when the QC24N modern function is not used

| kem | | Specifications | | | | |
|-----------------------|---------------------------|--|---|--|--------------------|--------------------------------------|
| | | | AJ71QC24N | | | AJ71QC24N-R4 |
| Inte | face | CH1 | RS-232C | RS-232 | | RS-422 |
| ĺ | | CH2 | RS-422/485 | RS-232 | | RS-422/485 |
| Con | nmunicati | ons method | (Only BS-2 | ull-duplex/Hai 232C interfa | lf-dupl ce is s | ex selectable.) |
| Syn | chronous | system | | top synchro | | |
| (uni | nsmission I: bps) | | 38400, 19200, 96 115200, 57600, 2 (selectable when 115200 bps.) | 8800, 14400 |), | |
| Data | format | Start bit | | 1 | | |
| | | Data bit | | 7/8 | | |
| | | Parity bit | | 1 (yes) / 0 | (no) | |
| | | Stop bit | | 1/2 | | |
| Епо | r | Parity check | Y | es (odd / eve | | o . |
| dete | detection Sum check | | Yes/No | | | |
| Tra | smission | DTR/DSR | Yes (Only RS-232C interface is selectable.) / No | | | |
| coni | nol 🤅 | DC code | Yes (DC1 / DC3, DC2 / DC4) / No | | | |
| Writ | ing to EE | | 100,000 times for the same area (Max.) | | | |
| i ne co | Indepe- ncient mode | Dedicated protocol Non- procedure protocol Bidirectiona i protocol | RS-232C RS-422 RS-422/485 Only 1: 1 can b protocol. m:n ca protocol. | 1: 1 1: 1, 1: n, m: e used for the | e brdi | rectional a dedicated |
| n n e | Interlock mode | Dedicated protocol | 1:n, m:n | | | QC24N-R2 is |
| c t i | | Non- procedure protocol | 1:n disabled to communic | | | J |
| ĥ | | | | | | |
| Transmission distance | | | RS-232C1 RS-4221 RS-422/4851 | 200m (3946- 98.4ft.) when | Oft) o GPP | r less (Within 30m is connected.) |

| ltem | Specifications | | | | |
|------------------------------|----------------|------------------------------------|--------------|--|--|
| | AJ71QC24N | AJ71QC24N AJ71QC24N-R2 AJ71QC24N-F | | | |
| Power consumption (5 VDC) | 0.4A | 0.3A | 0.6A | | |
| Number of I/O points | 32 ponts (*1) | | | | |
| Weight: ka (lb) | 0.385 (0.85) | 0.37 (0.82) | 0.385 (0.85) | | |

*1 Set special 32 points when allocating I/O by GPP function. Set *AJ71QC24" as a model name registration when using dedicated command.

2.2 When the modern function is used

The table below lists the transmission specification between QC24N and modern/terminal adapter on the QC24N side (abbreviated as TA from here on) when the QC24N modern function is used.

The transmission specification items not shown in the table shall be the same as those listed in Section 2.1

| tte | m. | Specifications | | | | |
|---------------------------------------|---|--|-----------------|----------------|--|--|
| | | AJ71QC24N | AJ71QC24N-R2 | | | |
| Applicability of m | odem function | . Us | | Unusable | | |
| Interface that car modern function | n be used | RS-232C RS-232C (*1) | | | | |
| Interlock mode b and CH2 for QC | | | Unusable | | | |
| Communications | s system | | Full-duplex | | | |
| Synchronous sy | stem | Start-s | top synchronous | system | | |
| Transmission sp | æed (unit: bps) | 38400, 19200, 9600, 4800, 2400, 1200, 115200, 57600, 28800, 14400, (Selectable when the total of CH1 and CH2 must be within 115200 BPS) | | | | |
| Transmission co | ntrol | RS/CS control Yes/No (Selection) | | | | |
| Applicability of | Dedicated protocol | | mmunication ene | | | |
| data communication | Non-procedure protocol | | mmunication ena | | | |
| | Bidirectional protocol | Communication enabled | | | | |
| | Communication with link dedicated instruction | Communication disabled | | | | |
| Line connection (QC24N: Mode | m) | | 1:1 | using either o | | |

*1 Communication by the modem function is possible using either of the two RS-232C interfaces. However, communication with the peripheral devices for GPPQ is possible only on the CH1 side.

3. Name of Each Part and Setting

Name of each part and setting switches in the QC24N are described.



*1 Seal showing the module hardware version and software version.



*2 The DATE column in the rated plate shows the manufacture date and function versions of the module. (Example)

Manufacture date (in case of April 1998 in western calendar)

(1) LED display

QC24N LED display shows the data communication status, operation status and error occurrence status for QC24N



LED No, in the left diagram are for explanation. They are not written on the actual module.

| LED | No. | LED | Meaning of | LED ON | LED OFF | Initial | Rela | ed Pro | tocol |
|--------------|---------|------------|--|---|--|------------------|-------|--------|-------------------|
| CH - 1 | CH 2 | Name | LED Display | (ON/BLINK) | (OFF) | Status of LED | Dedi- | Proce- | Bidine: tionel |
| ľ | 0 | RUN | Normal Operation | Normal | Abnomal | ON T | | 0 | |
| | 1 | CPU R/W | Communicating with PLC | Blinks during PLC com (when not communical | | OFF | | | |
| 2 10 4 | 8 đ | : 1 | (Not used during data communications.) | | | 770 | | _ | |
| 5 | 21 | NEŲ. | CHC neutral status | Transmission sequence initial status (Waiting to receive command messages) | Command message receive completed | *1 | 0 | - | |
| 6 | 22 | | CHI2 (Normal End) transmission | After (Normal end) transmitted | Afler [Abnonnai and] zahamilted | 0FF | | | ļ |
| 7 | 8 | NAK | CHID (Abnorma) End) transmission | Atter (Abnormal end) transmitted | Attar [Normal and] transmitted | OFF | | | 1 |
| 8 | 24 | CAN | CHE) and PLC CPU communications result | See *2 | Normai | OFF | | | |
| 9 | 8 | P/S | CHID parity/sum check error | Parity/sum check error | Normal | OFF | | 0 | |
| 10 | 26 | PRO. | | Communications protocol error | Normal | OFF | 0 | - | - |
| 11 | 27 | SIO | CHI SIO error | Overrun, framing error | Normai | OFF | | 0 | |
| | | | | When receive data purged because OS area is full. | Normal | OFF | - | Š | 2 |
| 12 | 28 | | (Unavailable) | · · · · | | OFF | | | |
| 13 | 8 | SD.WAFT | Send wait status | When data send wait state generated | After start of transmission | OFF | | 0 | _ |
| 14 | 30 | SO | CHD send status | Blinks during data transmission | Not sent | OFF | | | |
| 15 | 31 | RD | CHO receive status | Blinks during data reception | Not raceived | OFF | | |) |
| 16 | 1 | CHI ERA | CH1 error occultence | Switch setting error, mode switching error, send | Normal | OFF | ł | | |
| - | 17 | CH2 ERA | CH2 error occurrence | error, necerva error, on- idemand arror | Normal | OFF | | | |
| 1 | 8 | SW ERR. | Switch setting error | Switch setting error | Normal | OFF | | | |

- *1 The NEU, LED (LED No. 5, 21) come on when the objective interface is set to a dedicated protocol. When the objective interface is set to a protocol other than a dedicated protocol, these LEDs go off.
- *2 The C/N LED (LED No. 8, 24) come on in the following cases:
 - When incorrect communication requests was received from the external device.
 - * When an external device issued a data write request to the PLC CPU when transmission specifications switch "SW07" is set to off (write during RUN disable).
 - When access between the QC24N and PLC CPU generated an error.

| | Point |
|-----|---|
| (1) | When there is an error in the QC24N switch setting, the "SW |
| | ERR." LED turns on, and the "CH1 ERR."/"CH2 ERR." LED on |
| 1 | the corresponding interface side of the error turns on. |
| 1 | When the "SW ERR." LED is on, review the switch settings on |
| 1 | the interface side where the setting error occurred. |
| (2) | When the "SW ERR." LED is on, the data communication |
| | cannot be performed between the QC24N and external device. |
| l | (Both interfaces of the QC24N are disabled.) |

(2) Station number switch setting

Set station numbers for the external device to specify the PLC to access during dedicated protocols data communications.

| Station Switch Details | Description |
|------------------------|--|
| | Station number is set from 0 to 31. (Do not set a station number over 32.) X10 sets the station number 10 digit. X1 sets the station number 1 digit. Make sure that the station number setting does not overlap with another QC24N, etc., on the same network. Not necessary to set the station numbers an connect order. Station numbers can also be skipped. |

(The factory setting is [00].)

(3) Mode switch setting Set the data communications function of each interface.

| Mode Switch Details | Mode Switch No. | Setting Contents | | | | |
|------------------------|--------------------|---|-------------|--------------------|--|--|
| | 0 | When CH1 and CH2 operation is linked: 0 is set to CH1 and 1 to 6 are set to CH2. When CH1 and CH2 operate independently: Setting impossible. | | | | |
| | 1 | Dedicated | ASCII mode | Format 1 | | |
| | 2 | protocol | | Format 2 | | |
| l | 3 | ť | ļ | Format 3 | | |
| | 4 | 1 | | Format 4 | | |
| | 5 | 1 | Binary mode | Format 5 | | |
| 1 | 6 | Non procedure | e protocol | | | |
| | 7 | Bidirectional protocol | | | | |
| | 8toD | Setting impossible | | | | |
| | E | ROM/RAM/switch test | | | | |
| | F | Individual station loopback test | | | | |
| | | | | ctory setting: "1" | | |

actory setting.

| Point | |
|-----------------|---|
| Always set " | * to "7* for the mode setting switch on the interface |
| side that is no | t connected to the external device. |

(4) Transmission specifications switch setting Set the transmission specification for the communication with the corresponding device.

| Switch | Switch | Setting Item | Switc | n State | Notes |
|---|--------------------|---|---------------------------|---------------------|--|
| Details | CH1 CH2 | - | OFF | ON | |
| 00000000 \$= ## 3 3 3 5 5 00000000000000000000000000000 | SW01 | Operation setting | independent. operation | Linked operation | Always set CH1 to OFF ON/OFF can be set on CH2. |
| | SW02 | Data bits setting | 7 bits | 8 bits | Parity bit not included. |
| | SW03 | Parity bit enable /disable setting | Disable | Enable | When set to Enable, the setting of SW04 is effective. |
| | SW04 | Even parity /odd parity setting | Odd | Even | When Parity Bit Enable is selected. |
| 1 | SW05 | Stop bit setting | 1 bit | 2 bits | |
| | SW06 | Sum check enable/disable setting | Disable | Enable | Dedicated protocol, bidirectional protocol |
| f | SW07 | Write during RUN enable/disable setting | Disable | Enable | Dedicated protocol |
| l | SW08 | Setting change enable/disable | Oisable (prohibit) | Enable (allow) | Sets mode switching and EEPROM write allow/prohibit. |
| | SW09 to SW12 | Transmission rate setting | ۲ | ۳) | Can be set as long as the total of CH1 and CH2 is within 115200 BPS. |

(Factory settings are all OFF.)

*1 The data transmission speeds allowed to set are as follows:

| \sim | | | Transmission rate (unit: BPS) | | | | | | | | | | |
|--------|------|-----|-------------------------------|------|------|------|------|-------|--------|-------|-------|-------|--------|
| 1 | - | 300 | 600 | 1200 | 2400 | 4800 | 9600 | 19200 | 38400 | 14400 | 28800 | 57600 | 115200 |
| Switch | SW09 | OFF | ON | OFF | ON | OFF | ON | OFF | Š | OFF | ON . | OH- | N N |
| | SW10 | OFF | OFF | ON | ON | OFF | OFF | ON | | θĒ | OFF | Ň | ON |
| | SW11 | OFF | OFF | 0Ħ | OFF | ON | ON | ON | ÔN . | OFF | OFF | ĥ | 10 |
| L | SW12 | OFF | OFF | OFF | OFF. | OFF | OFF | OFF_ | ę Ŧ | Š | QN | ON | ON |

* Settings other than above are not accepted.

4. Mounting and Installation

This section describes the handling precautions and installation environment common to all the modules when handling the QC24N from unpacking to installation.

See the User's Manual of the PLC CPU module used for a detailed description of mounting and installation of the module.

4.1 Handling precautions

This section describes the module handling precautions.

- The module case is made of plastic. Be sure not to drop it or subject it to strong vibration.
- (2) Tighten the module mounting screws within the following tightening torque range.

| Screw | Tightening Torque Range |
|---|--|
| RS-422/485 terminal block terminal screws (M3.5 screws) | 59 to 88N + cm (6 to 9kgf + cm) (5.2 to 7.8 to + nch) |
| Module mounting screws (M4 screws) | 78 to 118N ⊷cm (8 to 12kgf ⊷cm) (6.9 to 10.4 bb ⊷inch) |
| RS-422/485 terminal block mounting screws (M3 screws) | 39 to 59N - cm (4 to 6kgf - cm) (3.5 to 5.2 lb - inch) |
| RS-232C and RS-422 connector mounting screws (M2.6 screws) | 19 to 24N cm (1.9 to 2.4 kgf • cm) (1.7 to 2.0 to • nch) |

4.2 Installation environment

Do not install the QnA Senes PLC in the following environments.

- (1) Where the ambient temperature exceeds the 0 to 55°C range.
- (2) Where the ambient humidity exceeds the 10 to 90% RH range.
- (3) Where condensation is produced by sudden temperature changes.
- (4) Where corrosive or combustible gas is present.
- (5) Where dust, iron powder and other conductive powder, oil mist, salt, or organic solvents are prevalent.
- (6) In direct sunlight.
- (7) Where a strong electric or magnetic field is generated.
- (8) Where vibration and shock may be transmitted directly to the module.

5. External Wiring

5.1 RS-232C connection

| 1 0 014 | Pin No. | Signa) Code | Signal Name | Signal Direction QC24N (*1) |
|---------|---------|-------------|---------------------|--------------------------------|
| 4 016 | 1 | FG (*2) | Frame ground | |
| 018 | 2 | SD (TXD) | Send data | |
| 7 019 | 3 | RD (RXD) | Received data | 4 |
| ST 021 | 4 | RS (RTS) | Request to send | |
| 100 022 | 5 | CS (CTS) | Send possible | |
| 110 024 | 6 | DSR (DR) | Data set ready | |
| 130 025 | 7 | SG | Signal ground | → |
| | 8 | CD | Carrier detection | |
| | 20 | DTR (ER) | Data terminal ready | |

^{*1} AJ71QC24N: CH1 side, AJ71QC24N-R2: CH1 side - CH2 side

*2 FG of the QC24N is connected to the nut used to fastern the connector, thus it is the FG of the module itself.

The following type of the RS-232C connector is used. The counter connector must match this connector.

25-pin D-sub (female) screw type

- 17L-10250-27-D9AC (DDK ELECTRONICS LTD)
- (1) An example of connecting to an external device which is capable of turning ON/OFF the CD signal (pin 8)

| QC24N | sidø | Cable connection and | External device |
|-------------|---------|---|-----------------|
| Signal name | Pin No. | Signal direction (Full-duplex/Half-duplex) | Signal name |
| FG | 1 | ★→ | FG |
| SD (TXD) | 2 |] | SD (TXD) |
| RD (RXD) | 3 | 4 | RD (RXD) |
| RS (RTS) | 4 | <u></u> | RS (RTS) |
| CS (CTS) | 5 | 74 | CS (CTS) |
| DSR (DR) | 6 | \sim | DSR (DR) |
| SG | 7 | ▮਼ | SG |
| CD | 8 | | CD |
| DTR (ER) | 20 | Γ | DTR (ER) |

- (2) An example of connecting to an external device which is not capable of turning ON/OFF the CD signal (pin 8)
 - (a) An example of external wrining for DC code control or DTR/DSR control

| QC24N | | Cable connection and | External device |
|-------------|---------|-----------------------------------|-----------------|
| Signal name | Pin No. | signal direction (Full-duplex) | Signal name |
| FG | | ····· | FG |
| SD (TXD) | 2 | | SD (TXD) |
| RD (RXD) | 3 | · | PID (FIXD) |
| RS (RTS) | 4 | ╏────┤ | RS (RTS) |
| CS (CTS) | 5 | ┦┥──│ └──⋗ | CS (CTS) |
| DSR (DR) | .6 | | DSR (DR) |
| SG | 7 | | SG |
| CD | 8 | | CD |
| DTR (ER) | 20 | | DTR (ER) |

(b) An example of external wring for DC code control

| QC24N side | | Cabal connection and signal direction | External device |
|-------------|---------|--|-----------------|
| Signal name | Pin No. | (Full-duplex) | Signal name |
| FG | 1 | | FG |
| SD (TXD) | 2 | 7 | SD (TXD) |
| RD (RXD) | 3 | | RD (RXD) |
| RS (RTS) | 4 | ॏ───┤ ा───┤ | RS (RTS) |
| CS (CTS) | 5 | ╏╉╌╌┦╴╴└╌╌╞ | CS (CTS) |
| DSR (DR) | 6 | Ĩ╡──┐ ┌──┣Ĭ | DSR (DR) |
| SG | 7 | ┦┫╌┼╌╌╌╌┼╌╼┝┤ | SG |
| CD | 8 | 1 { { { { { { { } } { { } { } { } { } { | 00 |
| DTR (ER) | 20 | ┨ <u>─</u> ───┛└───┤ | DTR (ER) |

(3) Precaution when performing connections

1) Handle the FG signal and the shield of the connection cable in the following manner.

| | Connection method | Remark |
|-----------|--|---|
| FG signal | Connect to the FG signal on the QC24N side. | Do not short circuit the FG signal and the SG signal of the connection cable. |
| Shield | Connect to the FG signal on the QC24N side. (Do not connect to an external device.) | When the FG signal and the SG signal are internality connected on the external device side, do not connect the FG signal of the QC24N to the external device. |

- When a normal data communication cannot be performed because of external noise even though the wiring has been made as above, perform the wiring as follows:
 - Connect between the FG of both stations with the shield of the connection cable.
 On the external device side, however, follow the instruction manual of the external device.
 - Connect each signal other than SG and FG of the connection cable by paring up with SG.



- * FG of the QC24N is connected to the nut used to fasten the connector, thus it is the FG of the module itself.
- 3) Do not connect a RS-422 device to the RS-232C interface. If a RS-422 device is connected, the RS-422 interface hardware on the connected device will be damaged, and communication will be disabled.

Point

When connecting QC24N and the modem/TA, use the RS-232C cable supplied with the modem/TA for a cable specified for the modem/TA during the modem function for QC24N is used.

5.2 RS-422/485 connection

The standard method for connecting the RS-422/485 line is shown below:

| Signal Code | Signal Name | Signal Direction QC24N (*1) + External Device |
|-------------|-------------------|---|
| SDA | Send data (+) | │ |
| SDB | Send data (-) | [> |
| RDA | Received data (+) | |
| RDB | Received data (-) | |
| SG | Signal ground | ▲> |
| (FG) | Frame ground | ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ |
| (FG) | Frame ground | |

*1 AJ71QC24N: CH2 side, AJ71QC24N-R4: CH2 side

(Function block diagram for the QC24N side)



(Function block diagram for the UC24).

Point

If the QC24N serves as the first or the last station on the RS-422/485 line, connect a terminal resistor as shown below to the RS-422/485 interface according to the communication specification. When a terminal resistor is not connected, an error may result during data communication.

- For RS-422 communication ------ 330 Ω, 1/4W
- For RS-485 communication ------ 110 Ω, 1/2W

(To be continued to the next page)

| Point | |
|------------------------------------|--|
| (1) When an connect a | external device and the QC24N are connected in 1: 1, a terminal resistor between RDA and RDB. |
| connect a | external device and the QC24N are connected in 1: n, a terminal resistor between SDA and SDB as well as RDA and RDB. |
| connect a | external device and the QC24N are connected m: n, a terminal resistor between RDA and RDB. a the following winng diagram represents a terminal |
| resistor. | |

(1) Example of connecting external devices and QC24N by 1:1

| | External device | Cable connection and | QC24N |] |
|----|-----------------|----------------------------|-------------|---|
| | Signal name | signal direction (example) | Signal name |] |
| ച | RDA : | ∢ −√√−1 | SDA | |
| R. | RDB | ≼ ^ | SDB | - |
| | SDA | ┝──╱⋯⋯⋯⋯⋯√──▶[| RDA | |
| | SDB | ┝╾╾╱┈┈┈╱╶╸┢ | RDB | Ŀ |
| - | RSA | | | |
| | RSB | | | |
| | CSA | ┝╉───│───┘ [| | |
| | CSB | k— [| | 3 |
| | | | SG | J |
| | SG | ∖ | (FG) | |
| | FG | ∖ | (FG) | Ţ |

(2) Example of connecting external devices and QC24N by 1: n
 1) Connecting the external device and QC24N modules via RS-232C





(4) Countermeasure for data reception errors in the external device with the RS-422 or RS-422/485 connection

During data communication with external devices via QC24N RS-422/485 interface, if there is a possibility that the external RS-422 or device receives an error data, install pull-up and pull-down resistors to the external device side (about 4.7kΩ, 1/4 W as a reference of resistor value).

Installation of pull-up and pull-down resistors will prevent data reception errors.



Point

Installation of pull-up and pull-down resistors will prevent data reception errors.

Remarks

The following explains the case in which pull-up and pull-down resistors are not installed to the external device:

 When none of the stations are transmitting, the transmission line is in a state of high impedance, causing the transmission line to become unstable due to noise and a possibility that the data will be received incorrectly at the external device. When this happens, a parity error or framing error is likely to

occur. Therefore, skip the data when the error has occurred.

 The first data during data reception is fixed in the following cases.

Skip the data received prior to the first data as determined.

- When data communicating via the decicated protocol, the user selects the first data according to the mode and format used.
- When data communicating via the non-procedure protocol using user registered frames, the user selects the first data according to the user registered frames to register in QC24N.

- (5) Precautionary items when winng
 - When connecting the SG and FG signals of the OC24N to an external device, follow the specification of the external device.
 - Connect the connector cable shield to either one of the FG terminals on the connected device.
 - If data communication cannot be performed normally due to external noise even if the wining is done as described above, perform writing as follows:
 - Connect the FGs of both stations using the connector cable shield. As for the connection on the external device, follow the instruction manual for the external device.
 - Connect the (FG) of the QC24N side to the FG terminal at the power supply module of the station which has a QC24N installed, or to the FG terminal of the control panel on which the QC24N PLC is installed.
 - Connect nnA and nnB in each signal of the connector cable as a pair.



(FG)->To the FG terminal at the power supply module of the station which has a QC24N installed or the FG terminal of the control panel

Point

- In the explanation of the terminal resistor setting/connection in this section, when an RS-232C - RS-422 converter or other equipment is used for the device which serves as either of the line terminating stations, setting/connection for a terminal resistor is required on the converter.
- (2) The devices connected to the QC24N RS-422/RS485 interface must use all RS-422 or all RS-485, including 1-to-n and m-to-n connections.

5.3 RS-422 connection

1000012001200

| 14 | 'Pin No. | Signal Code | Signal Name | Signal Direction QC24N (*1) ◀➔ External Device |
|---------------|----------|-------------|--------------------------------------|--|
| 16 | 1 | FG | Frame ground | |
| 17 | 2 | RDA | Received data (+) | |
| ē | 3 | SDA | Send data (+) | |
| 5678992129245 | 4 | DSRA | Terminal ready acknowledgment (+) | → → → → → → → → → → → → → → → → → → → |
| 3 | 5 | DTRA | Data device ready (+) | |
| 5 | 7 | SG | Signal ground ("2) | · → |
| / | 8 | SG | Signal ground (*2) | <> |
| | 15 | RDB | Received data (-) | |
| | 16 | SDB | Send data (-) | |
| | 17 | DSRB | Terminal ready acknowledgment (-) | |
| | 18 | DTRB | Data device ready (-) | |
| | 20 | SG _ | Signal ground (*2) | $\leftarrow \longrightarrow$ |
| | 21 | SG | Signal ground (*3) | $ \rightarrow $ |

The standard method for Connecting the RS-422filne is shown below -

*1 AJ710C24N-R4: CH1 side

*2 Each SG is connected inside the module.

*3 Be sure to connect with SG of the opposite device.

The following type of the RS-422 connector is used. The counter connector must match this connector,

25-pm D-sub (female) screw type 17L-10250-27-D9AC (DDK ELECTRONICS LTD)

(Function block diagram for the QC24N side)



Point (1) For the AJ71QC24N-R4 RS-422 interface, do not connect an external device which requires power supply from the AJ71QC24N-R4. Doing so may cause module or external device malfunctions. (2) The terminal resistor connection or setting is required on the external device side. Perform the terminal resistor connection or setting according to the manual for the external device. There is no need to connect the terminal resistor on the QC24N side because it contains the terminal resistor. The R in the following wiring diagram represents a terminal resistor.

 An example of external winning for DC code control or DTR/DSR control

| QC24N side | | Cabal connection | External device | |
|----------------|---------|---|-----------------|------------------------|
| Signal name | Pin No. | and signal direction (example) | Signal | Terminator position |
| FG | 1 | ▲ > | FG | |
| RDA | 2 |] ♦-~, | SDB | 1 |
| RDB | 15 |] ←~^^_[| SDA | |
| SDA | 3 | ┠╧╌╱┄┄┄╌╱╼┢[| RD6 | |
| SD8 | 16 | ╷ ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,, | RDA | ┠──╘╬┙ |
| DSRA | 4 | ▲ ~ | DTRB | |
| DSRB | 17 | ┨╉╾╱┄┄┄╌╱─╌┦ | DTRA | |
| DTRA | 5 |]—∼∕√→[| DSRB | |
| DIRB | 18 | <u>]∕∕.→[</u> | DSRA | ┝──╘╩┙ |
| SG | 7 | ┫√ | SG | |
| SG | 8 | ┫┓┛┙ | SG |] |
| SG | 20 | | SG | Ţ |
| SG | 21 | | SG | |

| OC24N side | | Cable connection | External device | |
|------------|---------|-------------------------------------|-----------------|------------------------|
| Signal | Pin No. | and signal direction ((example) | Signal | Terminator position |
| FG | 1 | A | FG | Ţ |
| RDA | 2 | ▝₦─৵┈┈┈⋎──□ | SDB | 7 |
| RDB | 15 | Ĩ ₄ ₋∧∧[| SDA | 3 |
| SDA | 3 | ┨──╱······╱─ ▶ [| RDB |] |
| SDB | 16 | °∔∕\∕→□ | RDA | |
| DSRA | 4 |]≪ ∽, ┍—{ | <u>89770</u> | 3 |
| DSRB | 17 | ╠╉╌ ╀╼┑ ╓╼╀╼╼ <u>[</u> | DTRA |] |
| DTRA | 5 | ⋽⊷╴┤ ┤└╺┝[| DSRB | |
| OTRB | 18 | ┠╍╍┙ └╍┢[| DSRA | |
| SG | 7 |]╉-~, | SG | |
| SG | 8 |]₊₋∧ | SG | 3 |
| SG | 20 |]┥▶[| SG |] |
| ŚG | 21 |]╉──┘ [| SG | 1 |

(2) An example of external wiring for DC code control

- (3) Connection precautions
 - 1) See Section 5.2 (4) for the countermeasures for erroneous receive data on the external device side.
 - Connect the QC24N DTR
 terminal and DSR
 terminal as shown in this section.
 - Connect the shield of the connection cable to either side of the connected device. When connecting to the QC24N side, connect to the FG signal.
 - 4) When a normal data communication cannot be performed because of external noise even though the wiring has been made as above, perform the wiring as follows:
 - Connect the shield of the connection cable to both stations.

QC24N side : Connect the shield to the connector or to the chassis.

(Example) Connect to the (FG) terminal of the RS-422/485 interface.

External device side: Connect according to the instruction manual of the external device

(Example) Connect to the FG terminal, etc.

 Connect nnA and nnB of each signal in the connection cable as a pair.



- 5) Make the connections according to the QC24N RS-422 interface and connected device signal polarity specifications. (Connection of the □A and □B signals to the QC24N may be the opposite of those described in this section.)
- 6) Do not input RS-232C signals to the RS-422 interface. If RS-232C signals are input, the hardware of the RS-422 interface may be damaged and communications may be lost.

6. External Dimensions

(AJ71QC24N (-R2, R4))

Unit mm (inch)



R2 (bend radius near connector) r1 (bend radius near connector) Cable diameter × 4 Cable diameter × 4 Can be connected within the range over which bending is not excessive

* Except for the interface section, the AJ71QC24N (-R2, R4) external dimensions of all three models are the same.

The illustration above shows the external dimensions of the AJ71QC24N.

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